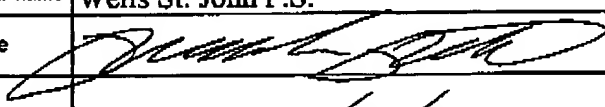


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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/652,550
	Filing Date	August 31, 2000
	First Named Inventor	Keiji Jono
	Group Art Unit	2811
	Examiner Name	Thien Tran
Total Number of Pages in This Submission	Attorney Docket Number KM1-001	

ENCLOSURES (check all that apply)		
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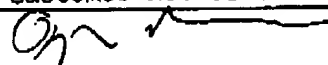
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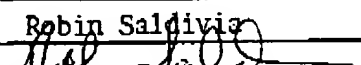
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<h1>TRANSMITTAL FORM</h1> <p>(to be used for all correspondence after initial filing)</p>	Application Number	09/652,550	
	Filing Date	August 31, 2000	
	First Named Inventor	Keiji Jono	
	Group Art Unit	2811	
	Examiner Name	T. Tran	
Total Number of Pages in This Submission	14	Attorney Docket Number	KM1-001

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Firm or Individual name	Frederick M. Fliegel, Ph.D. - Reg. No. 36,138 Wells, St. John, Roberts, Gregory & Matkin P.S. Customer No. 021567
Signature	
Date	October 26, 2001

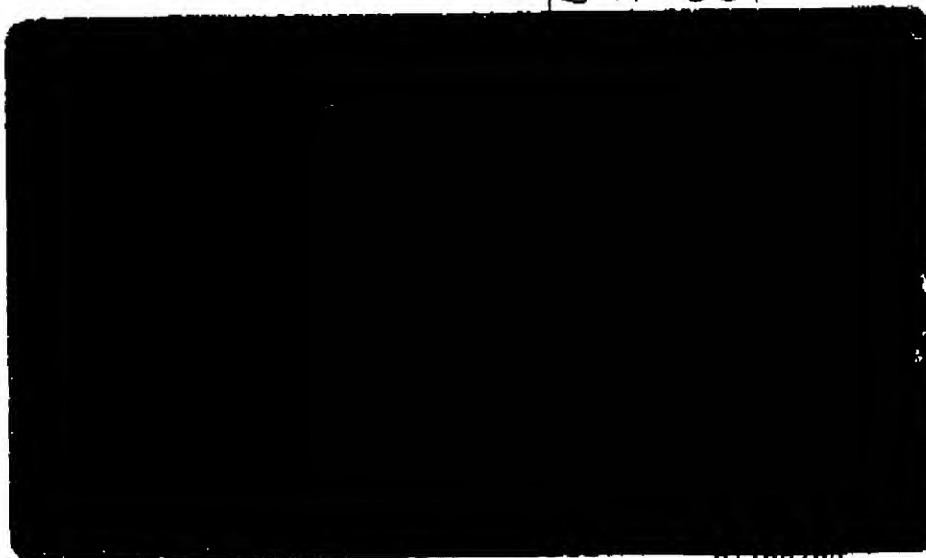
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October 26, 2001
KMI-001
KMT Semiconductor, LTD & Micron Technology, Inc.
The stamp of the U.S. Patent and Trademark Office acknowledges receipt of the following papers relating to the Patent application of Keiji Jono et al, Serial No. 09/652,550, filed August 31, 2000, entitled "Methods of Forming an Isolation Trench in a Semiconductor, Methods of Forming an Isolation Trench in a Surface of a Silicon Wafer, Methods of Forming an Isolation Trench-Isolated Transistor, Trench-Isolated Transistor, Trench Isolation Structures Formed in a Semiconductor, Memory Cells and DRAMS".

1. PTO Return Postcard Receipt.
2. Transmittal Form (PTO/SB/21).
3. Response to October 12, 2001 Office Action.

KMI-001



01/09/02